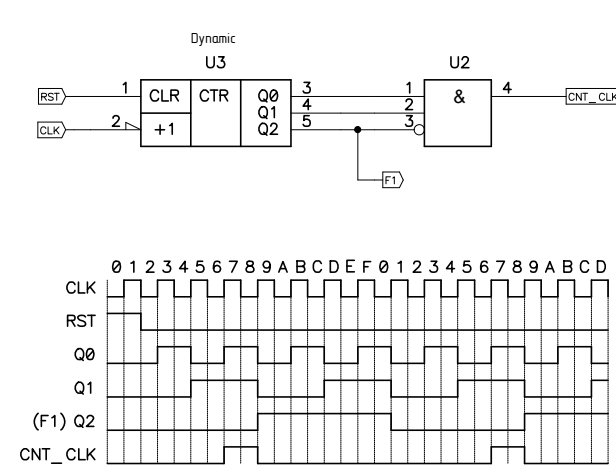
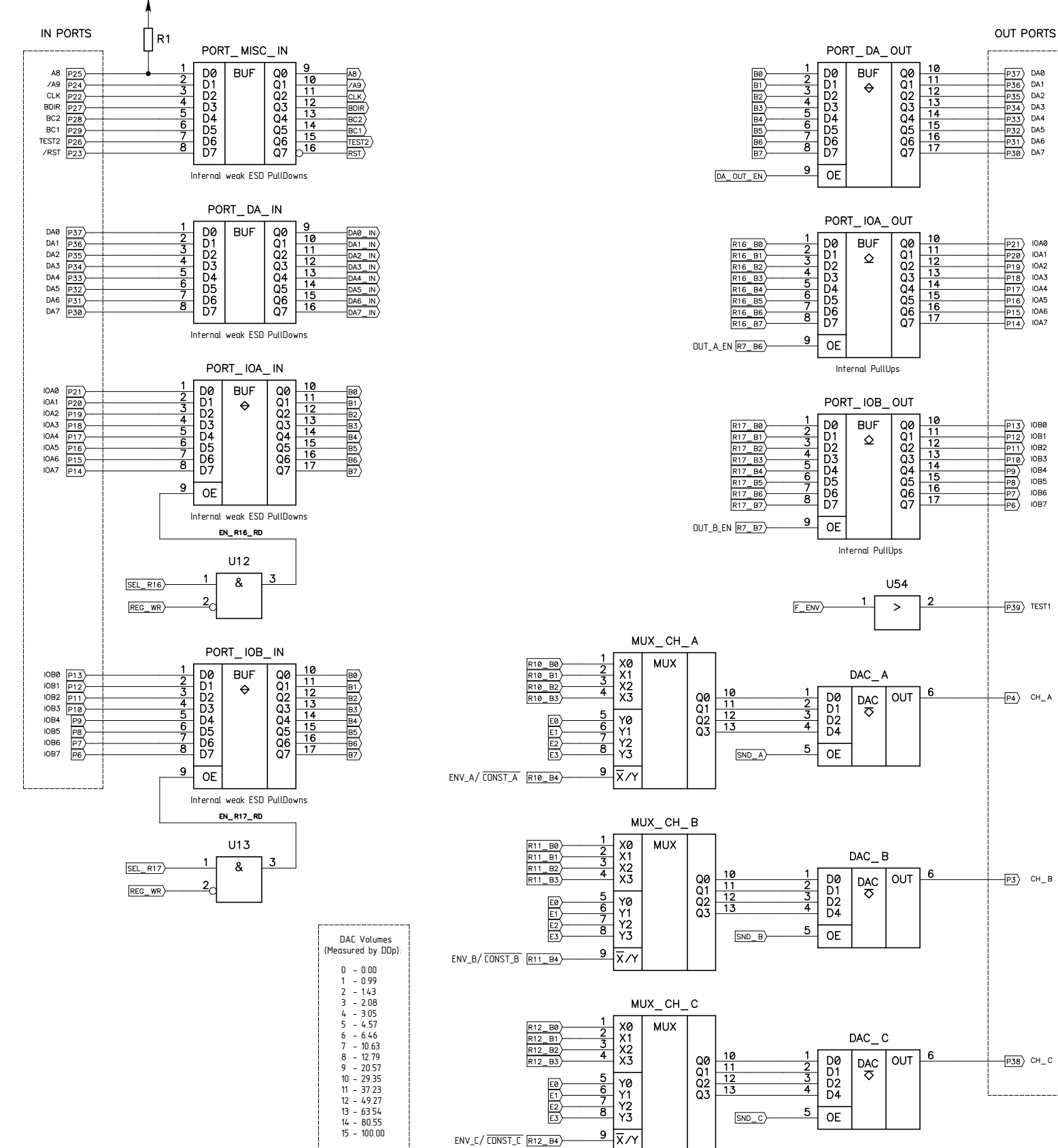


AY-3-8910  
1979 G.I. CORP  
90-320033-100

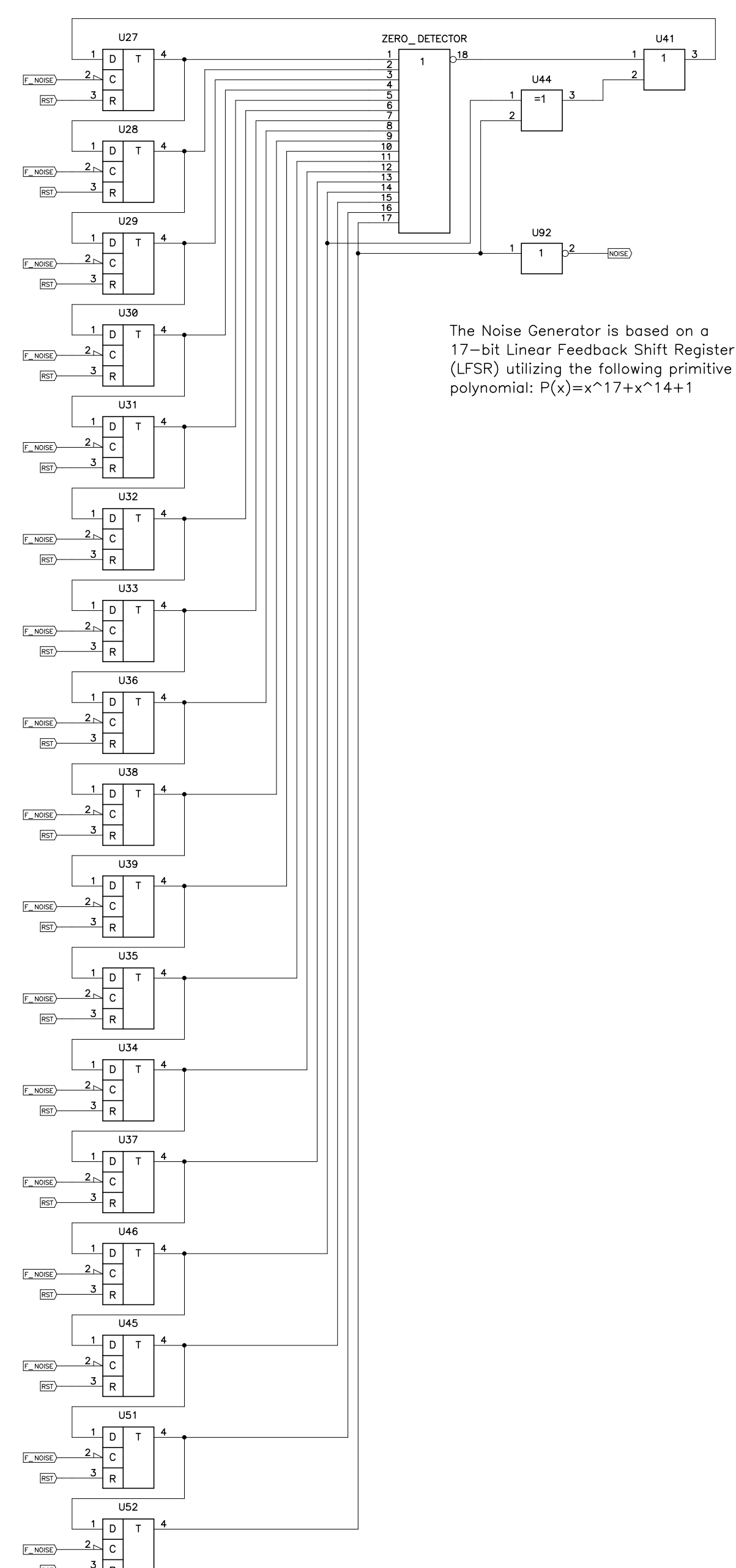
# CLOCK GENERATOR



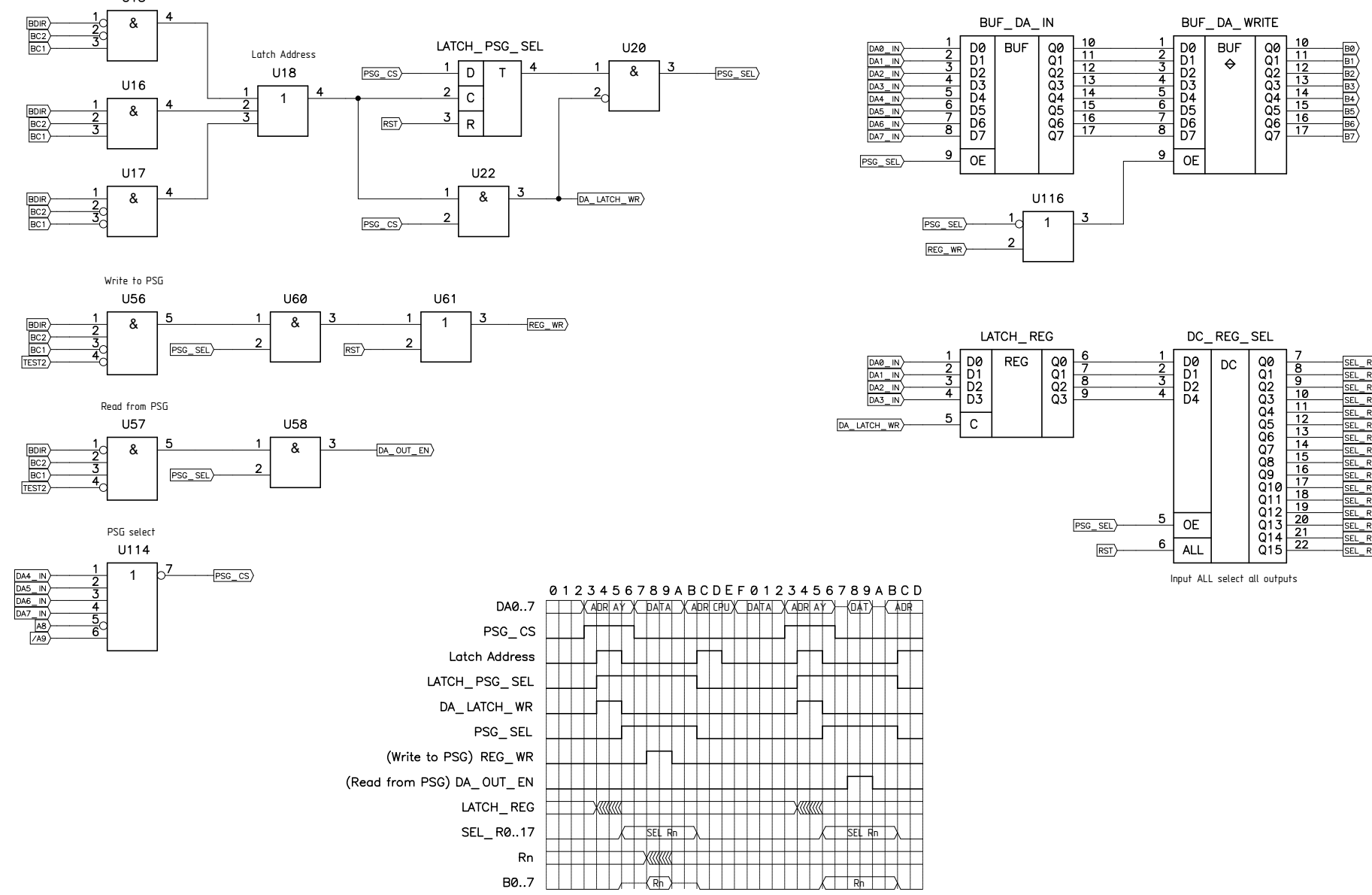
## I/O PORTS



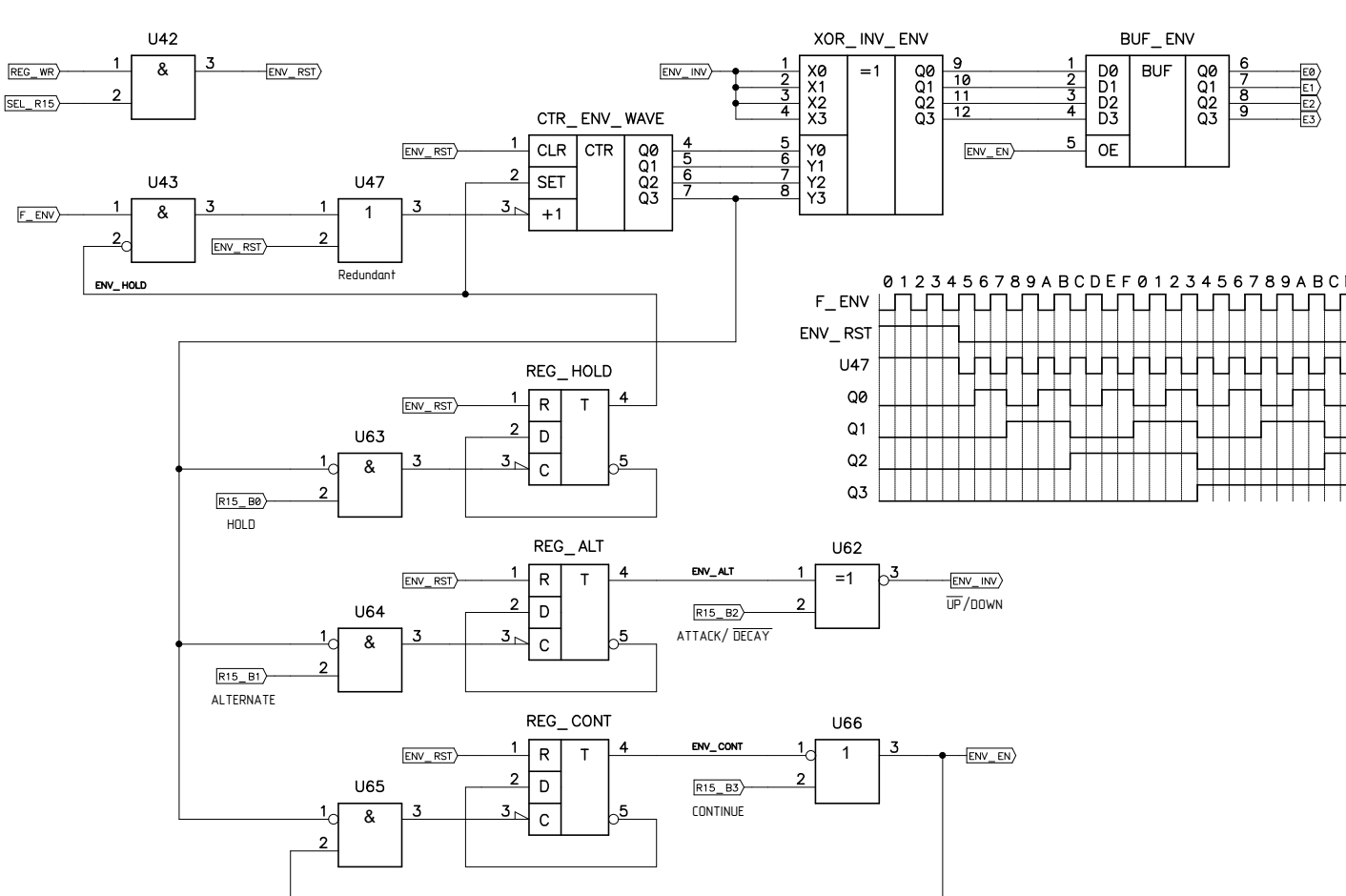
## 17-BIT LFSR NOISE GENERATOR



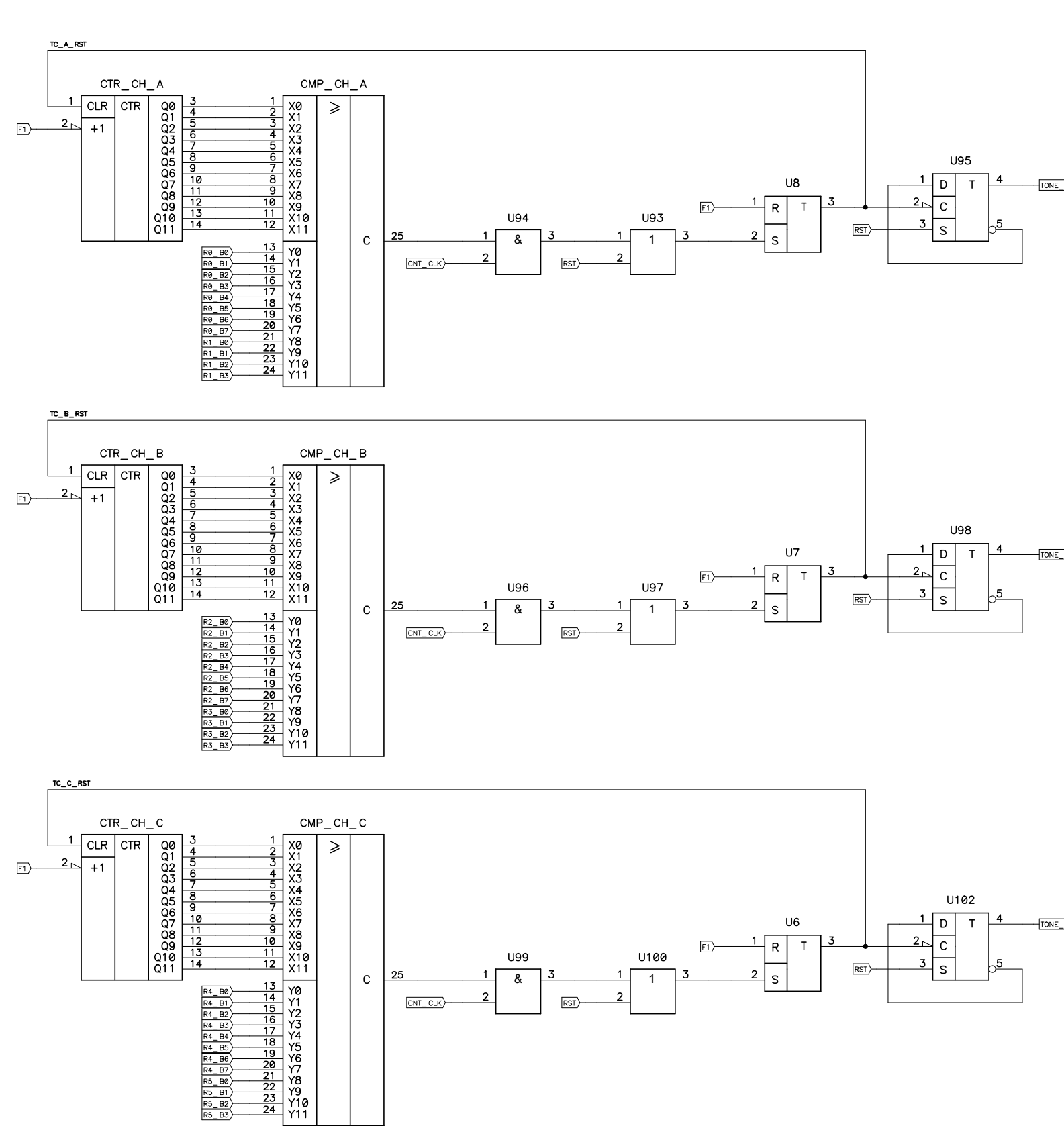
## I/O CONTROL LOGIC



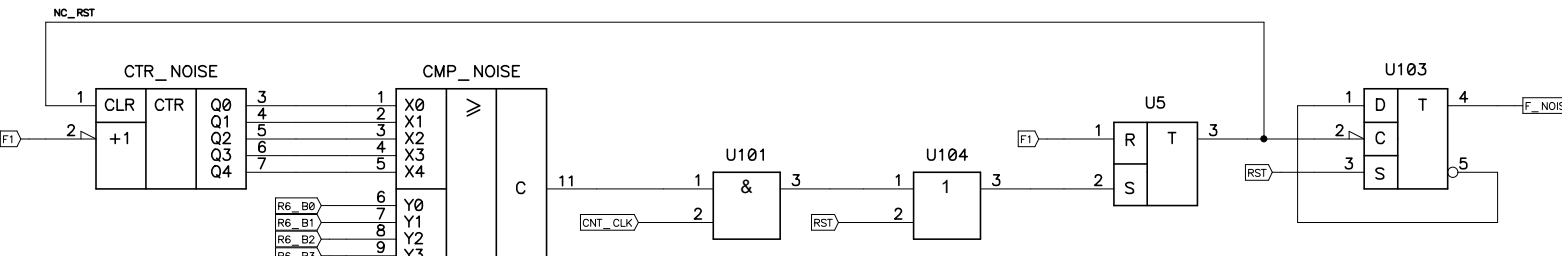
# ENVELOPE GENERATOR



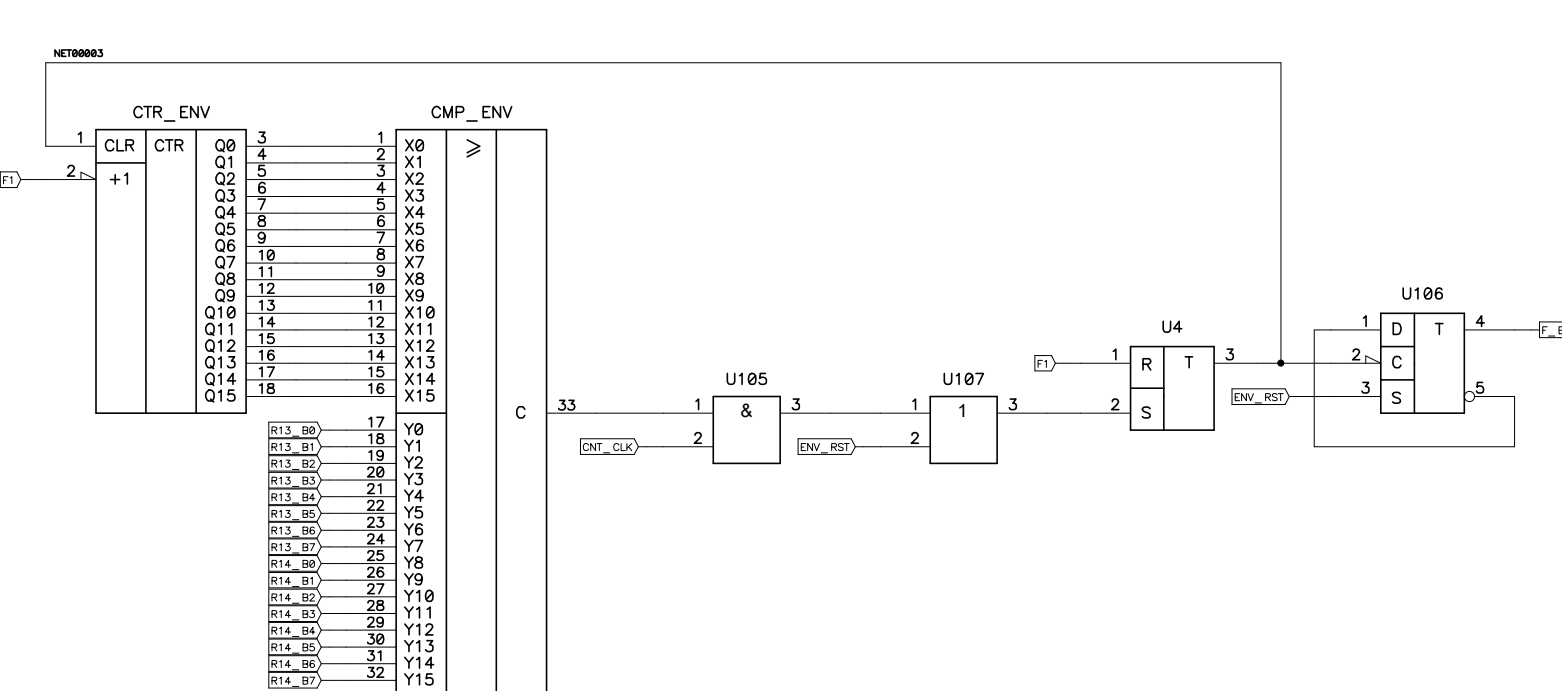
## TONE COUNTERS



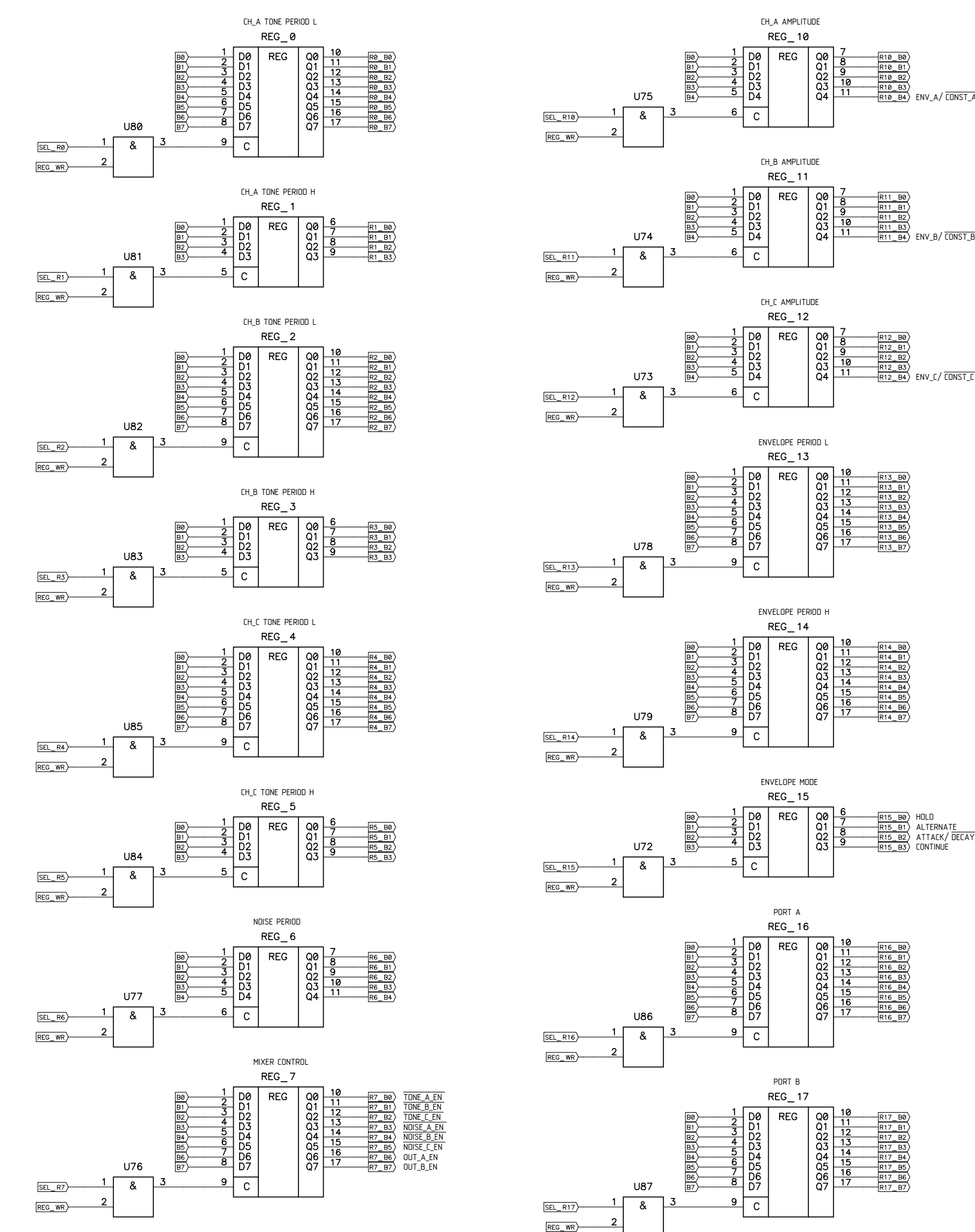
# NOISE COUNTER



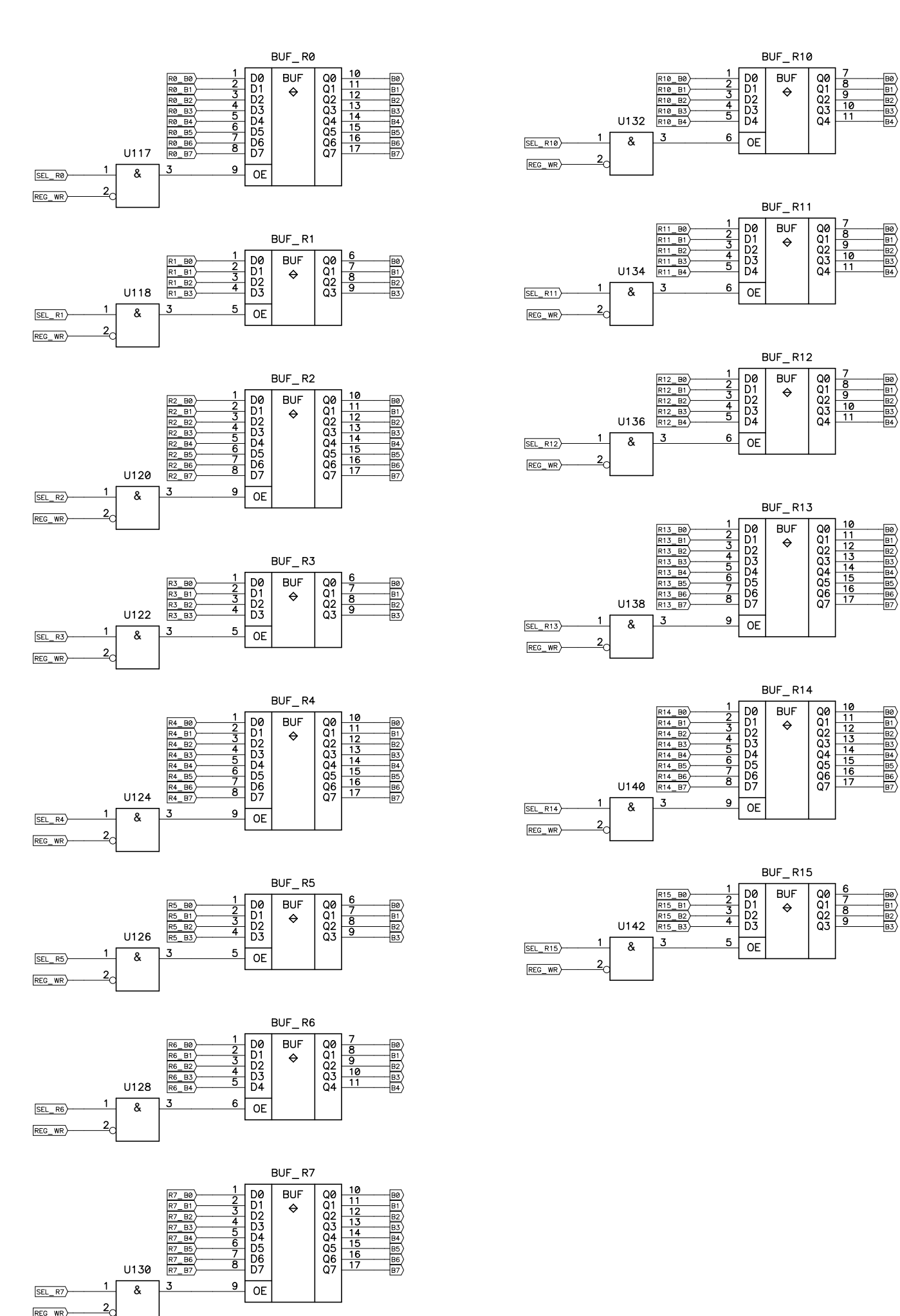
## ENVELOPE COUNTER



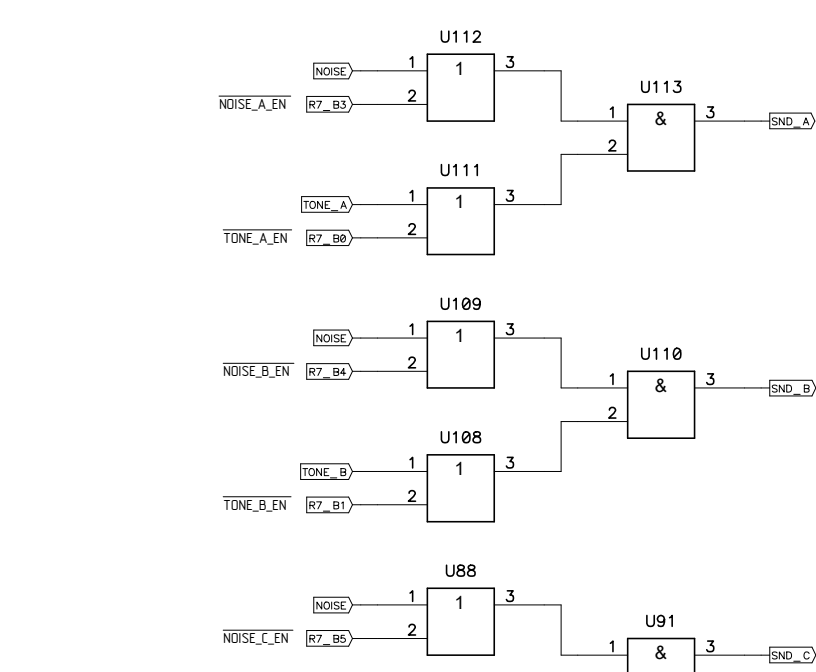
## REGISTERS



## READ BUFFERS



## TONE/NOISE MIX



Transistor RE by deathsoft (2019)  
Logic schematic by dr.Titus (2025)